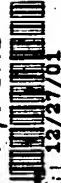


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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10032861	FILING DATE 12/27/2001	CLASS 351	SUBCLASS 118.000	GAU 2936	EXAMINER Va
**APPLICANTS: Fukuoka Michio; Hasegawa Kenichi; Nagatomo Yasuki; Hatanoka Eizo; Tokada Hideyuki; Iwao Toshiyuki;					
**CONTINUING DATA VERIFIED: <i>No</i>					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2000-397685 12/27/2000 <i>ck</i> JAPAN 2000-397686 12/27/2000 <i>ck</i> JAPAN 2001-183173 06/18/2001 <i>oh</i>					
PG-PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials <i>BAK</i>				ATTORNEY DOCKET NO MAT-8219US	
TITLE : Circuit protector					

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NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	Total Claims
			Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Primary Examiner	Sheets Drwg. Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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